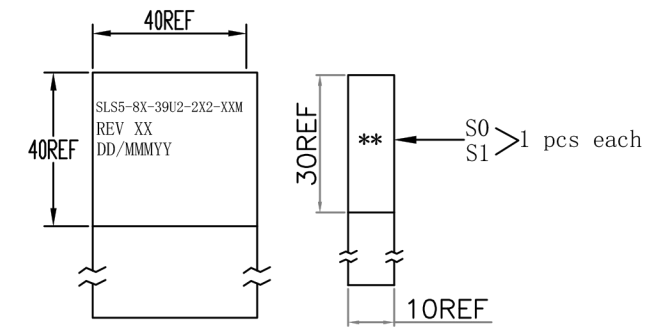
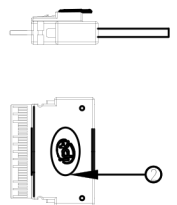
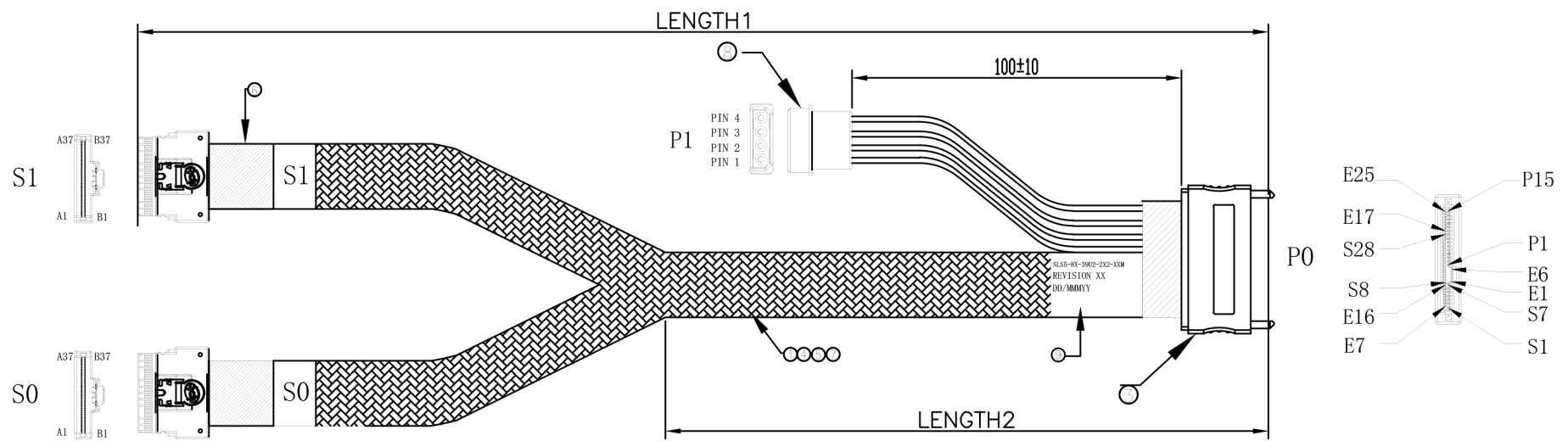


DATE	DESCRIPTION OF CHANGE	REV
2021-07-07	Wiring definition optimization	G
2021-07-09	STRUCTURAL MODIFICATION	H



SC PART NUMBER	LENGTH1(MM)	LENGTH2(MM)
SLS5-8X-39U2-2X2-0.5M	500±20	300±20
SLS5-8X-39U2-2X2-1M	1000±30	600±30

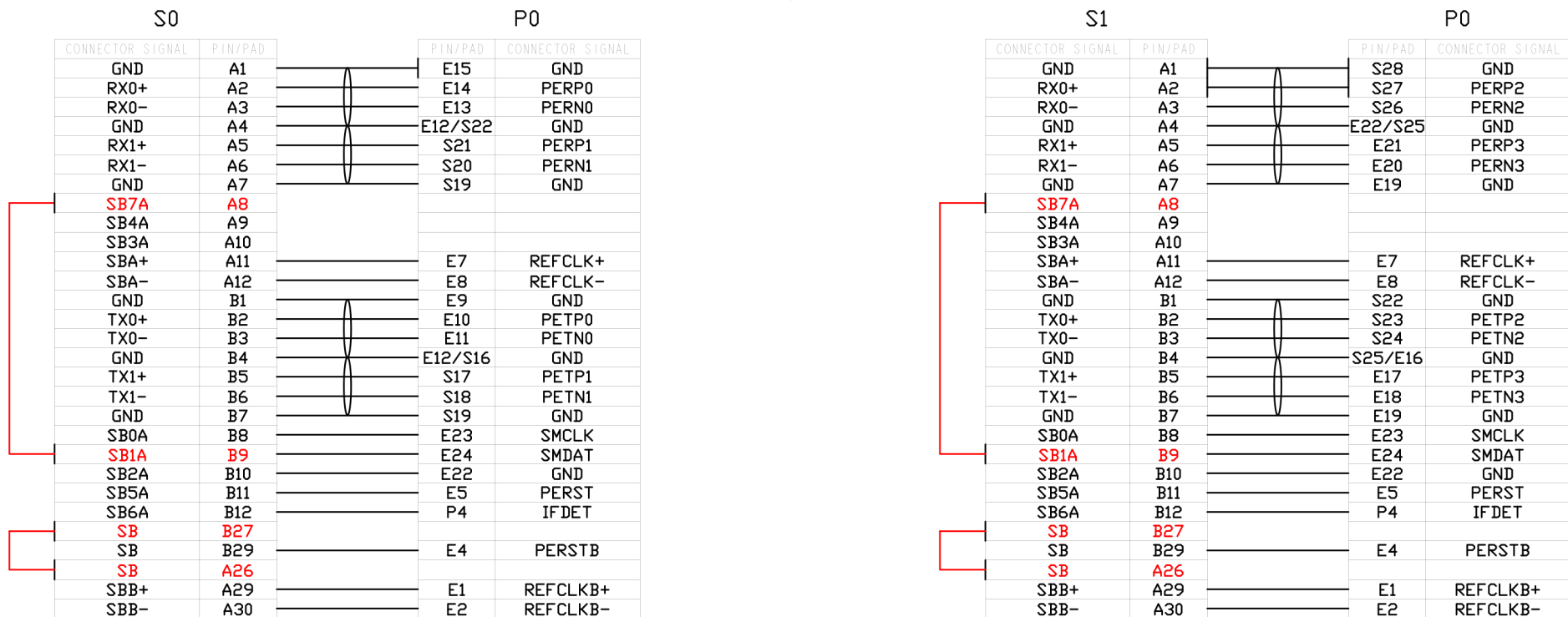
- TEST CONDITIONS:**
- 100% OPEN & SHORT & MISS-WIRING & INTERMITTENCE TEST
 - CONDUCTIVE RESISTANCE: 3Ω MAX
 - INSULATION RESISTANCE: 10 MΩ
 - HI-POT TEST: 300VDC FOR 10ms.
 - THIS PRODUCT MEETS PCIE 5.0

NO.	NAME	IPT NO.	DESCRIPTION	Q'TY	Q'TY
⑩	LABEL		WHITE LABEL, L W=10*30 S0 or S1	2	PCS
⑨	LABEL		LABLE : 40*40MM	1	PCS
⑧	Connector		HSG 5.08 4P	1	SET
⑦	WIRE		WIRE:UL1007 18AWG 41C BLK/RED/YEL	-	MM
⑥	Adhesive tape		IERMLENE CLOTH TAPE, T=0.11mm. BLK.	-	MM
⑤	Network manager		EXPANDO TUBE: OD=7MM, BLACK, VW-1,	-	MM
④	WIRE		WIRE 1571 30AWG STRANDED	-	MM
③	Connector		SFF-8639 PCIE 68POS CONN, STR, BLACK	1	SET
②	Connector		SAS FF-8654 8X STR With shrapnel	2	SET
①	CABLE		SAS 30AWG*2C+2D+A+HM PLATED, 85ohm	-	MM

TITLE						SHEET	1/2
PCIE GEN 5 SlimSAS 8i (SFF-8654 Str. toX2 MultilinkGen 5 PCIe Drive Receptacles (SFF-8639 68P, 2x2 connectivity, 0.5M length)							
CUSTOMER		CUSTOMER P/N	SLS5-8X-39U2-2X2-XXM	DRAWING	<i>lupeng</i>		
DWG NO.	WIPT20210511002	UNIT	SCALE	REV	VIEW	CHECKED	<i>lupeng</i>
IPT P/N		mm	NONE	H		APPROVAL	

DATE	DESCRIPTION OF CHANGE	REV
2021-07-07	Wiring definition optimization	G
2021-07-09	STRUCTURAL MODIFICATION	H

WIRING TABLE



1. E25 TO E22 SHORT The connector GND
2. E16,E15 OPEN

POWER WIRING TABLE					
POWER CONNECTOR	PIN	SYMBOL	CONNECTOR	PIN	COLOR
PI	1	12V	P0	P13, P14, P15	YELLOW
	2	GND		P12	BLACK
	3	GND		P6	BLACK
	4	5V		P7, P8, P9	RED

TITLE	PCIE GEN 5 SlimSAS 8i (SFF-8654 Str. toX2 MultilinkGen 5 PCIe Drive Receptacles(SFF-8639 68P, 2x2 connectivity, 0.5M length)						SHEET	2/2
CUSTOMER		CUSTOMER P/N	SLS5-8X-39U2-2X2-XXM		DRAWING	<i>lupeng</i>		
DWG NO.	WIPT20210511002	UNIT	SCALE	REV	VIEW	CHECKED	<i>lupeng</i>	
IPT P/N		mm	NONE	H		APPROVAL		