PCIe Gen4 U2/U3 8Bay Active JBOF

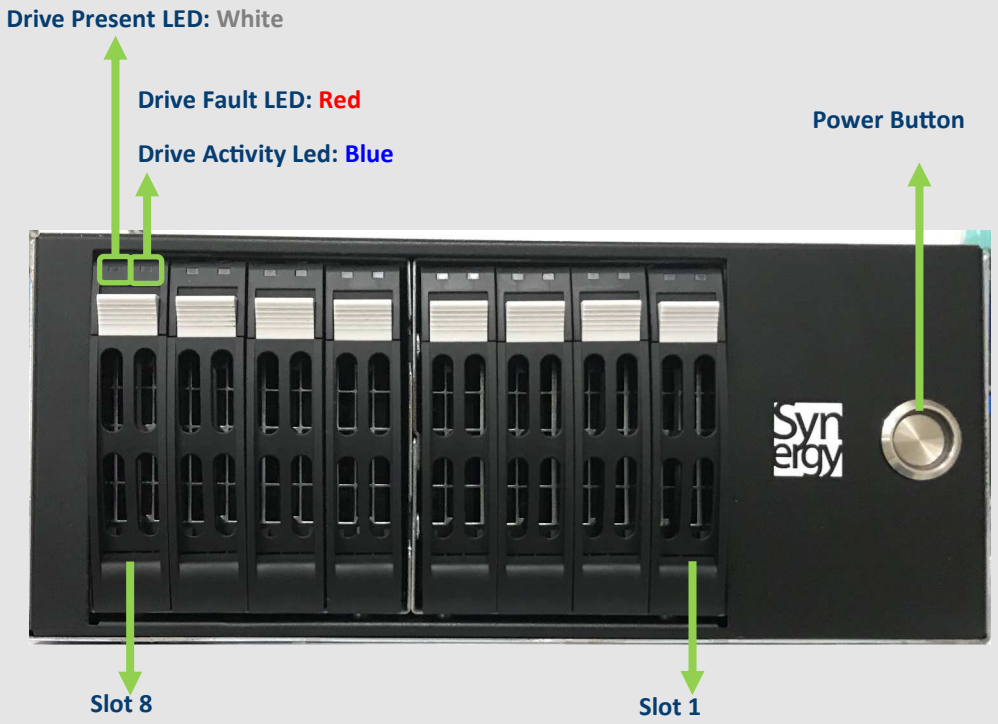
User's Manual

REV: 1.2

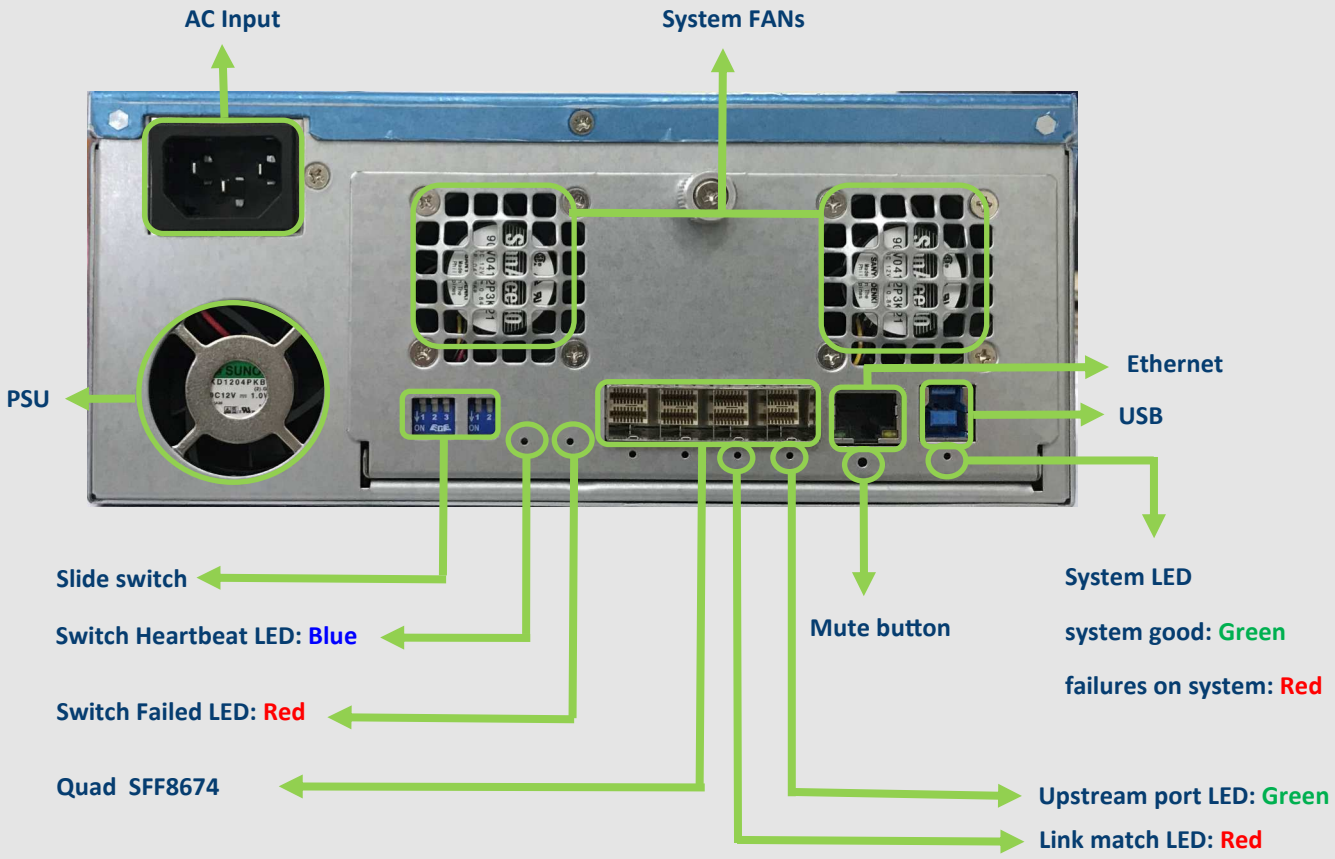
Oct. 2020

PCIe Gen4 U2/U3 8Bay Active JBOF

Front Panel

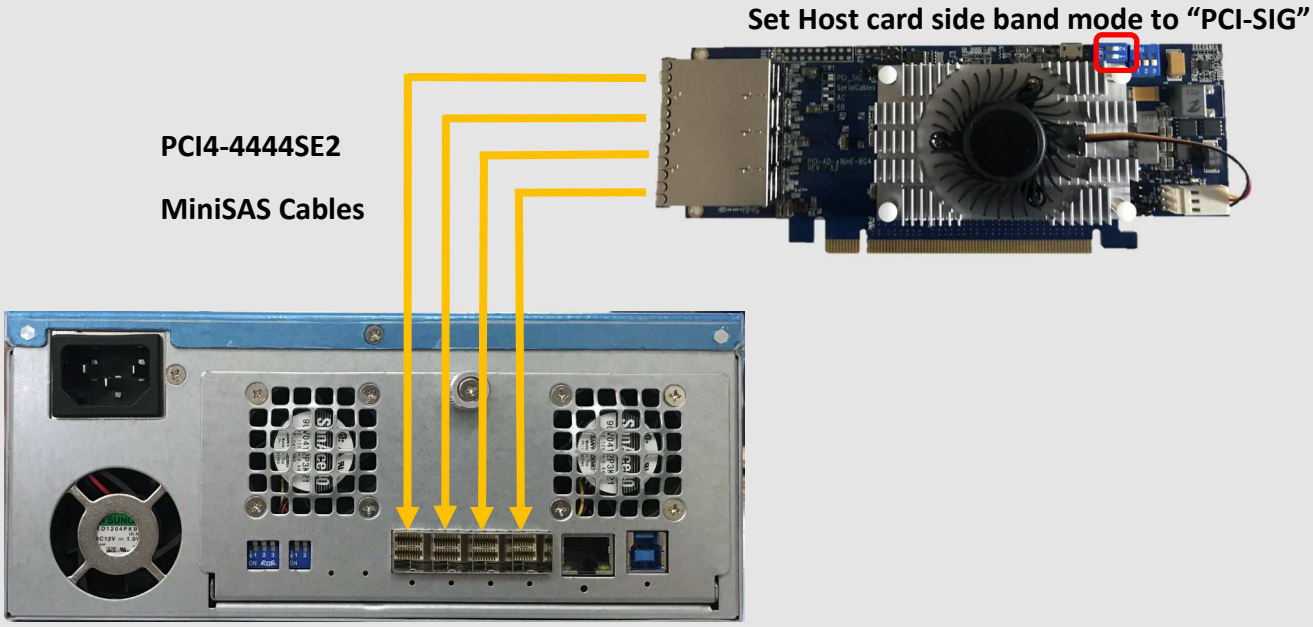


Rear I/O



Connecting JBOF to Host card

Synthetic JBOF: 1 Host



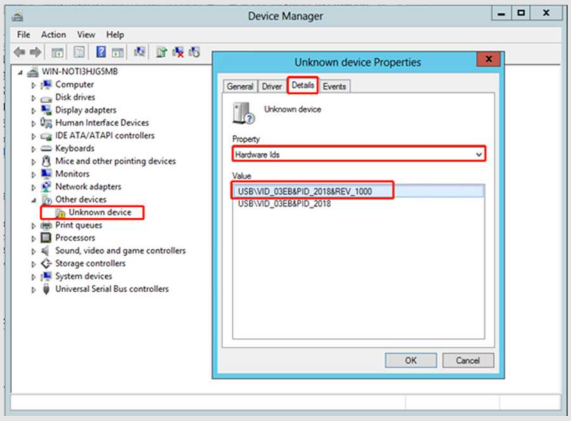
USB Driver Installation

Step1: Download and install the CDC driver for unidentified device (VID_03EB&PID_2018)

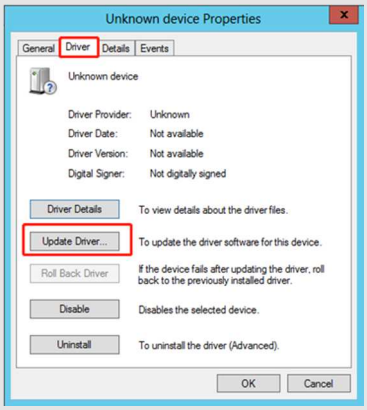
Available at:

https://www.serialcables.com/wp-content/uploads/2018/11/SynergyUSBCDC_20180518.rar

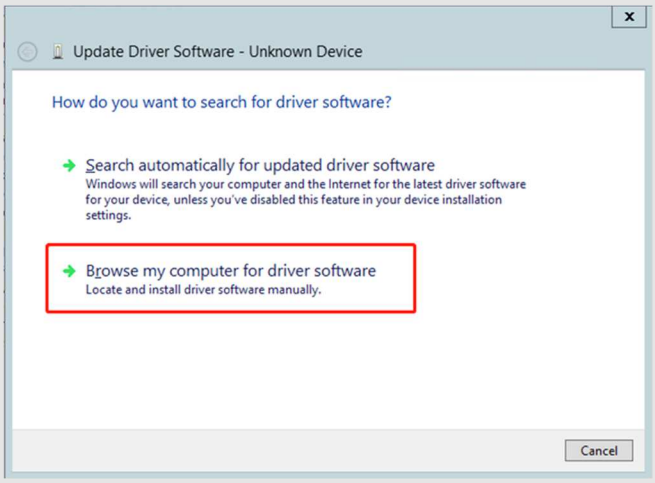
Note: No USB driver is required for Windows 10 and Linux



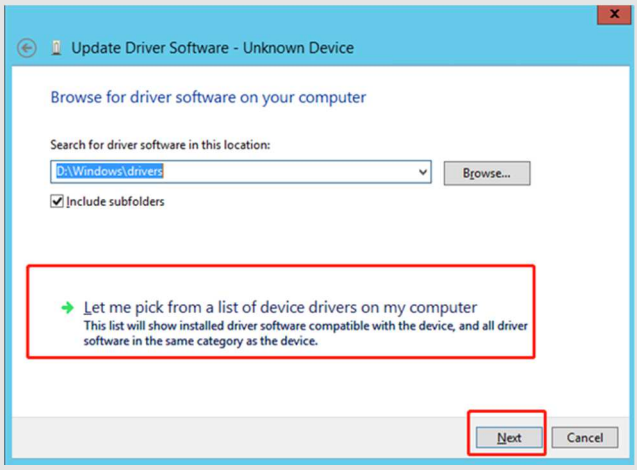
[Figure 1]



[Figure 2]

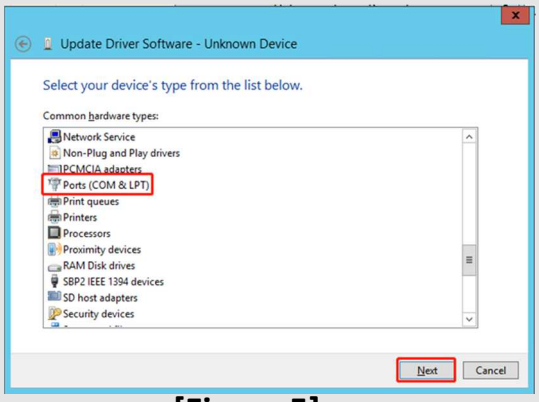


[Figure 3]

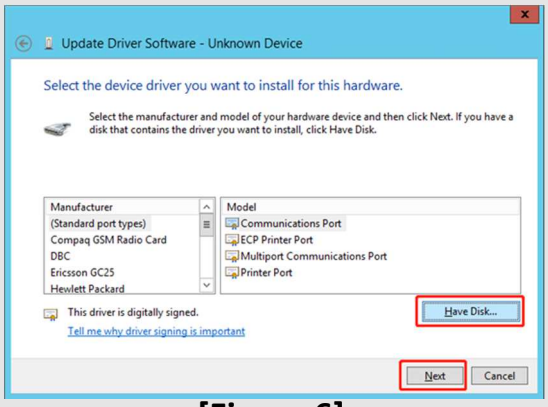


[Figure 4]

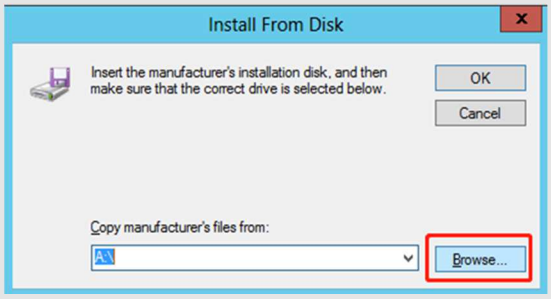
USB Driver Installation



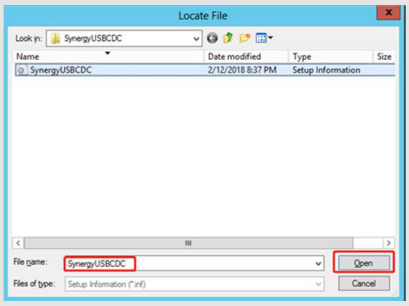
[Figure 5]



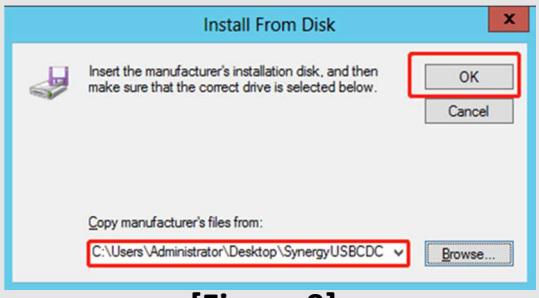
[Figure 6]



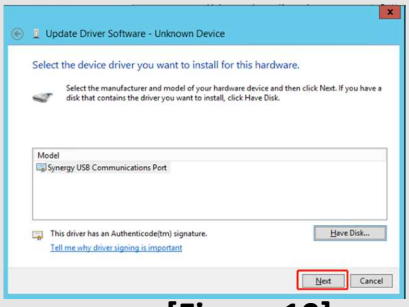
[Figure 7]



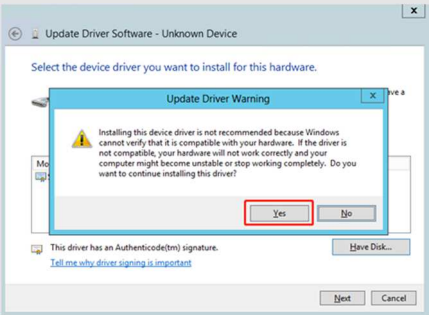
[Figure 8]



[Figure 9]



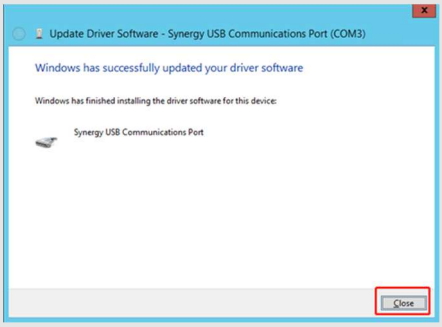
[Figure 10]



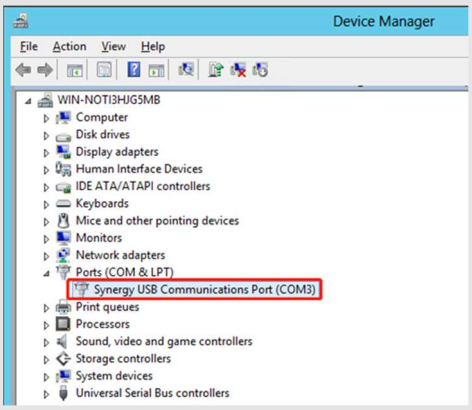
[Figure 11]

[Figure 12]

USB Driver Installation



[Figure 13]



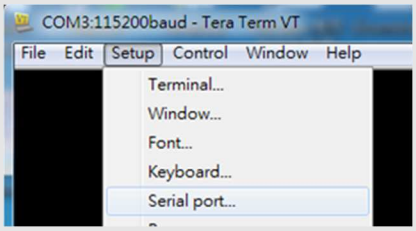
[Figure 14]

CLI Setup

Step 1. Install and launch Tera Term application

(or Hyper Terminal requires version 3.0 or higher).

Step 2: To ensure proper communications between Active 8bays JBOF controller and the VT100 Terminal emulation, please configure the VT100 Terminal emulation settings to the values shown below:



Step 3:

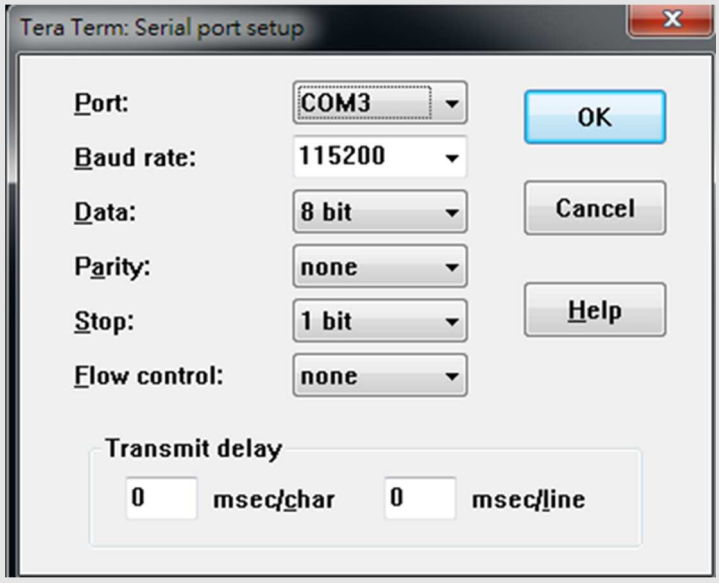
For "Port", select COM3 in this example. (Depend on which COM port used on Host)

For "Baud rate", select 115200.

For "Data", select 8 bit. For "Parity", select none.

For "Stop", select 1 bit. For "Flow control", select: none.

Click OK when you have finished your selections.



uP Synergy FW Upgrading

Step 1. Connect the USB port of JBOF to PC or laptop

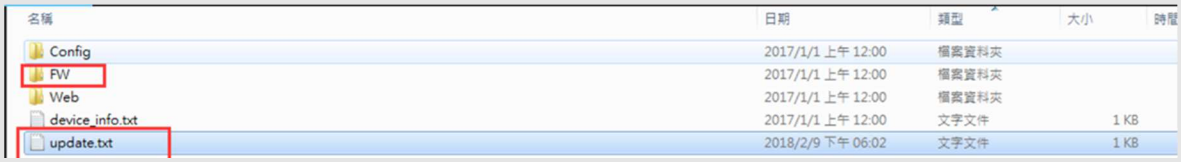
Step 2. Press the mute button in the rear of JBOF then power on.

Step 3.

a.) it will show an added USB device in PC or laptop.

b.) Put upgrading FW(i.e atals_gen4_8bays_jbof_v0.1.3.srec) into the folder of FW.

c.) Put update.txt in the root folder.



名稱	日期	類型	大小	時間
Config	2017/1/1 上午 12:00	檔案資料夾		
FW	2017/1/1 上午 12:00	檔案資料夾		
Web	2017/1/1 上午 12:00	檔案資料夾		
device_info.txt	2017/1/1 上午 12:00	文字文件	1 KB	
update.txt	2018/2/9 下午 06:02	文字文件	1 KB	

Step 4. Power cycle JBOF to apply the new FW.

Active JBOF Commands List

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>help
  Cmd Help Menu
    eth :
          Set Ethernet IP Configuration.
          - Usage: eth <ipaddr(*)> <subnet(*)> <gateway(*)>

    setmac :
          Set Ethernet MAC address.
          - Usage: setmac <xx:xx:xx:xx:xx:xx>

    fdl :
          Xmodem download image.
          - Usage: fdl <cfg|sbr|fw|mfg>
          - cfg : update config into MCU
          - sbr : update sbr into switch
          - fw  : update fw into switch
          - mfg : update mfg into switch

    lsd :
           Show environmental conditions information.
          - Usage: lsd

    pwmctrl :
          Fan pwm ctrl.
          - Usage: pwmctrl <fan_id(D)> <duty(D)|off>
          - fan_id(D) : fan_id should be 1 ~ 3
          - duty(D) : duty should be 0 ~ 100 (Fan1 duty 10 ~ 100)

    mw :
          Write 32-bit data to register.
          - Usage: mw <register(H)> <data(H)>
          - register(H) : register should be 0x00000000 ~ 0xFFFFFFFC
          - data(H) : data should be 0x00000000 ~ 0xFFFFFFFF

    dr :
          Dump switch-specific registers.
          - Usage: dr <register(H)> [count(H)]
          - register(H) : register should be 0x00000000 ~ 0xFFFFFFFC
          - count(H) : count should be 0x00000000 ~ 0xFFFFFFFC

    dp :
          Dump switch port-specific registers.
          - Usage: dp <port_number(D)>
          - port_number(D) : port_number should be 0 ~ 47

    df :
          Dump switch-specific flash.
          - Usage: df <address(H)> [count(H)]
          - address(D) : address should be 0x00000000 ~ 0xFFFFFFFC
          - count(H) : count should be 0x00000000 ~ 0xFFFFFFFC
    ssdpwr :
          slot power control.
          - Usage: ssdpwr [<slot(D)> <on|off>]
          - slot(D) : slot number should be 1 ~ 8

    ssdrst :
          Reset slot.
          - Usage: ssdrst <slot(D)|all> [channel(C)]
          - slot(D) : slot number should be 1 ~ 8
          - channel(C) : channel number should be a or b
          - Ex: ssdrst 1
          - Ex: ssdrst 1 a
          - Ex: ssdrst all
          - Ex: ssdrst all a

    showport :
          Display link speed and link width information.
          - Usage: showport

    showtype :
          Show backplane type.
          - Usage: showtype
```

```
    dual :
          Set dual channel enable on/off.
          - Usage: dual [<slot(D)|all> <on/off>]
          - slot(D) : slot number should be 1 ~ 8
          - Ex : dual all on
          - Ex : dual 1 on

    buz :
          buzzer control.
          - Usage: buz [on|off|en|dis]

    scan :
          Scan devices of I2C bus.
          - Usage: scan

    spread :
          Set PCIe clock spread.
          - Usage: spread [on|off]

    clk :
          Set PCIe clock output enable.
          - Usage: clk [en|dis]

    iicwr :
          iicwr <Addr(H)> <Slot(D)> <ReadByte(D)> <WriteData(H)>
          - Addr(H) : Device address
          - Slot(D) : Slot should be 1 ~ 8
          - ReadByte(D) : Max read byte is 32 byte
          - WriteData(D) : Max write byte is 32 byte
          - Ex : iicwr d4 1 8 0

    iicw :
          iicw <Addr(H)> <Slot(D)> <WriteData(H)...>
          - Addr(H) : Device address
          - Slot(D) : Slot should be 1 ~ 8
          - WriteData(D) : Max write byte is 32 byte
          - Ex : iicw d4 1 ff

    ver :
          Show microcontroller firmware version.
          - Usage: ver

    eventmask :
          Set System Event Mask.
          - Usage: eventmask [<number(D)> <on|off>]
          - number(D): number should be 1 ~ 7

    quit :
          Close telnet.
          - Usage: quit
    reset :
          System reset.
          - Usage: reset
```

eth Command

Set Ethernet IP configuration.

Usage: eth <ipaddr(*)> <subnet(*)> <gateway(*)>

```
File  Edit  Setup  Control  Window  Help

Cmd>eth 192.168.100.211 255.255.255.0 0.0.0.0

 Set Ethernet - save configuration ok
Cmd>
```

```
File  Edit  Setup  Control  Window  Help

Cmd>eth

==========================================================================
    Physical Address . . . . . . . . . . : 2E-09-0A-00-76-C7
    Ethernet Link Status . . . . . . . . : Up
    IP Address . . . . . . . . . . . . . : 192.168.100.211
    Subnet Mask  . . . . . . . . . . . . : 255.255.255.0
    Gateway  . . . . . . . . . . . . . . : 0.0.0.0
    MTU  . . . . . . . . . . . . . . . . : 1500
==========================================================================
```

setmac Command

Set Ethernet MAC (Media Access Control) address

Usage: setmac <xx:xx:xx:xx:xx:xx>

```
File  Edit  Setup  Control  Window  Help

Cmd>setmac 38:26:2B:00:00:00

 MacAddress[0] 38

 MacAddress[1] 26

 MacAddress[2] 2B

 MacAddress[3]  0

 MacAddress[4]  0

 MacAddress[5]  0

 Set MAC - save configuration ok
Cmd>
```

fdl Command

Update the configuration, SBR, FW or MFG files for Atlas PCIe switch.

The fld is used for CLI via USB Type-B port not support in RJ45 LAN port

Usage: fdl cfg|sbr|fw|mfg

```
File  Edit  Setup  Control  Window  KanjiCode  Help
    fdl :
         Xmodem download image.
         - Usage: fdl <cfg|sbr|fw|mfg>
         - cfg : update config into MCU
         - sbr : update sbr into switch
         - fw  : update fw into switch
         - mfg : update mfg into switch
```

cfg=update the combined config file into MCU, user can utilize “setmode” command to select any of config (support in the future)

sbr=update the SBR file for Atlas switch.

fw=program or upgrade FW for Atlas switch.

mfg=update mfg file for Atlas switch.

lsd Command

Shows environmental information (etc. temperature, fan, voltage) of Active 8bays JBOF.

Usage: lsd

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>lsd

Thermal:
   Switch Temperture 1 :  28 degree
Fan Speed:
            Switch Fan :  4994  rpm
           System Fan1 :  7617  rpm
           System Fan2 :  8108  rpm
Voltage Sensor:
           12V Voltage :  12272 mV
          1.8V Voltage :  1858  mV
          0.9V Voltage :  898   mV
```

pwmctrl Command

Set the PWM duty for all FANs in JBOF

Usage: pwmctrl <fan_id(D)> <duty(D)|off>

fan_id=1, Switch Fan

fan_id=2, System Fan1

fan_id=3, System Fan2

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>pwmctrl 2 100
Cmd>lsd

Thermal:
   Switch Temperture 1 :  35 degree
Fan Speed:
            Switch Fan :  5964  rpm
           System Fan1 :  15734 rpm
           System Fan2 :  10062 rpm
Voltage Sensor:
           12V Voltage :  12112 mV
          1.8V Voltage :  1858  mV
          0.9V Voltage :  898   mV
```

```
File  Edit  Setup  Control  Window  Help

Cmd>pwmctrl  1 off
Fan1: smart fan enable
Cmd>
```

Caution: The 8Bays JBOF implemented smart fan control, it isn't suggested to set FAN PWM manual unless for stress testing purpose

mw Command

Write 32bits data to registers

Usage: mw <register(H)> <data(H)>

register(H) : register should be 0x00000000 ~ 0xFFFFFFFC

data(H) : data should be 0x00000000 ~ 0xFFFFFFFF

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>mw fff0017c ffffffff
```

dr Command

Atlas switch registers dump per address (offset)

Usage: dr <register(H)> [count(H)]

```
File  Edit  Setup  Control  Window  Help

Cmd>dr 60800000

60800000:c0121000 00100000 060400a0 00010000
60800010:00000000 00000000 00000000 000001f1
60800020:0000fff0 0001fff1 00000000 00000000
60800030:00000000 00000040 00000000 00000100
60800040:c8034801 00000008 01866805 00010000
60800050:00010000 00000000 00000000 00000000
60800060:00000000 00000000 0052a410 00008004
60800070:00000810 00436d04 00010000 00000060
60800080:00001508 00000000 00000000 00050840
60800090:00000000 01800f1e 00010004 00000000
608000a0:00000000 0000000d a0481000 00000000
608000b0:00000000 00000000 00000000 00000000
608000c0:00000000 00000000 00000000 00000000
608000d0:00000000 00000000 00000000 00000000
608000e0:00000000 00000000 00000000 00000000
608000f0:00000000 00000000 00000000 00000000
Cmd>
```

```
File  Edit  Setup  Control  Window  Help

Cmd>dr 60800000 4

60800000:c0121000
Cmd>
```

dp Command

Atlas switch registers dump per port (offset 0x0 ~ 0xFFF)

Usage: dp <port_number(D)>

```
File  Edit  Setup  Control  Window  Help

Cmd>dp 0

60800000:c0121000 00100000 060400a0 00010000
60800010:00000000 00000000 00000000 000001f1
60800020:0000fff0 0001fff1 00000000 00000000
60800030:00000000 00000040 00000000 00000100
60800040:c8034801 00000008 01866805 00000000
60800050:00000000 00000000 00000000 00000000
60800060:00000000 00000000 0052a410 00008004
60800070:00000810 00436d04 00010000 00000060
60800080:00001508 00000000 00000000 00050840
60800090:00000000 01800f1e 00010004 00000000
608000a0:00000000 0000000d a0481000 00000000
608000b0:00000000 00000000 00000000 00000000
608000c0:00000000 00000000 00000000 00000000
608000d0:00000000 00000000 00000000 00000000
608000e0:00000000 00000000 00000000 00000000
608000f0:00000000 00000000 00000000 00000000
60800100:fb410003 00000000 00805e10 00000000
60800110:00000000 00000000 00000000 00000000
60800120:00000000 00000000 00000000 00000000
60800130:00000000 00000000 db410004 00000000
60800140:00000000 00000001 af410002 00000c00
60800150:00000000 00000000 03000004 800400ff
60800160:00000000 00000000 00000000 00000000
60800170:00000000 00000000 30201000 7f7f5040
60800180:7f28187f 7f7f7f7f 7f24147f 7f647f7f
60800190:7f2c1c7f 7f7f7f7f 7f7f7f7f 7f7f7f7f
608001a0:7f7f7f7f 7f7f7f7f 7f7f7f7f 7f7f7f7f
608001b0:7f7f7f7f 7f7f7f7f 7f7f7f7f 7f7f7f7f
608001c0:7f7f7f7f 7f7f7f7f 7f7f7f7f 7f657f7f
608001d0:7f7f7f7f 7f7f7f7f 7f7f7f7f 7f7f7f7f
608001e0:7f7f7f7f 7f7f7f7f 7f7f7f7f 7f7f7f7f
608001f0:7f7f7f7f 7f7f7f7f 00000000 00000000
```

df Command

Atlas switch registers of flash dump

Usage: df <address(H)> [count(H)]

```
File  Edit  Setup  Control  Window  Help

Cmd>df 400

00000400:c43d10c0 fc010000 d0010000 00000000
00000410:01000000 00000000 01000000 00000000
00000420:01000000 00000000 01000000 00000000
00000430:01000000 00000000 01000000 00000000
00000440:01000000 00000000 01000000 cc030000
00000450:80090000 00000000 00000000 00830090
00000460:24490200 00000000 00004c00 80a00015
00000470:00000000 00000000 a1c0a9c0 00000117
00000480:09091585 11040000 00000c00 00000000
00000490:00000000 00000000 00000000 00000000
000004a0:00000000 00000000 00000000 00000000
000004b0:00000000 0000c029 00009fe5 00f0a0e1
000004c0:03000000 00000000 00000000 00000000
000004d0:00000000 00000000 00000000 00000000
000004e0:00000000 00000000 00000000 00000000
000004f0:00000000 00000000 00000000 00000000
Cmd>
```

```
File  Edit  Setup  Control  Window  Help

Cmd>df 400 10

00000400:c43d10c0 fc010000 d0010000 00000000
Cmd>
```

ssdpwr Command

Slot power status checking and ON/OFF control

Usage: Usage: ssdpwr [<slot(D)> <on|off>]

```
File  Edit  Setup  Control  Window  Help

Cmd>ssdpwr
Backplane slot 01 power status turn off.
Backplane slot 02 power status turn off.
Backplane slot 03 power status turn off.
Backplane slot 04 power status turn off.
Backplane slot 05 power status turn off.
Backplane slot 06 power status turn off.
Backplane slot 07 power status turn off.
Backplane slot 08 power status turn on.
```

```
File  Edit  Setup  Control  Window  Help

Cmd>ssdpwr 8 off

Slot 08 turn off success.
Cmd>ssdpwr
Backplane slot 01 power status turn off.
Backplane slot 02 power status turn off.
Backplane slot 03 power status turn off.
Backplane slot 04 power status turn off.
Backplane slot 05 power status turn off.
Backplane slot 06 power status turn off.
Backplane slot 07 power status turn off.
Backplane slot 08 power status turn off.
```

```
File  Edit  Setup  Control  Window  Help

Cmd>ssdpwr 8 on

Slot 08 turn on success.
Cmd>ssdpwr
Backplane slot 01 power status turn off.
Backplane slot 02 power status turn off.
Backplane slot 03 power status turn off.
Backplane slot 04 power status turn off.
Backplane slot 05 power status turn off.
Backplane slot 06 power status turn off.
Backplane slot 07 power status turn off.
Backplane slot 08 power status turn on.
```

The slot power is turned off automatically when drive is plug out from slot, the use case of power control command is when drive is plugging into slot.

ssdrst Command

Issue PERST# from uP to device

Usage: Usage: ssdrst <slot(D)|all> [channel(D)]

Channel a: The 1st PHY of dual port drive

Channel b: The 2nd PHY of dual port drive

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>ssdrst 1
Reset slot 1 success
Cmd>
```

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>ssdrst 1 a
Reset channel a of slot 1 success
Cmd>
```

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>ssdrst all
Reset all slot success
Cmd>
```

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>ssdrst all b
Reset channel b of all slot success
Cmd>
```

showport Command

Shows link speed and link width information for all upstream ports(SFF8674) and downstream ports(SFF8639)

Usage: showport

```
File  Edit  Setup  Control  Window  Help

Cmd>showport
NVMe Slot-----------------------------------
Slot01: present  No, speed 01, width 00
Slot02: present  No, speed 01, width 00
Slot03: present  No, speed 01, width 00
Slot04: present  No, speed 01, width 00
Slot05: present  No, speed 01, width 00
Slot06: present  No, speed 01, width 00
Slot07: present  No, speed 01, width 00
Slot08: present Yes, speed 03, width 04
Ext. Slot-----------------------------------
Con. 01: speed 01, width 00, Type    Upstream
--------------------------------------------
```

When “dual” command is not implemented for slots, the showport shows the information for single port x4 per slot

```
File  Edit  Setup  Control  Window  Help

Cmd>showport
NVMe Slot-----------------------------------
Slot01: present  No, ch_1 speed 01, width 00, ch_2 speed 01, width 00
Slot02: present  No, ch_1 speed 01, width 00, ch_2 speed 01, width 00
Slot03: present  No, ch_1 speed 01, width 00, ch_2 speed 01, width 00
Slot04: present  No, ch_1 speed 01, width 00, ch_2 speed 01, width 00
Slot05: present  No, ch_1 speed 01, width 00, ch_2 speed 01, width 00
Slot06: present  No, ch_1 speed 01, width 00, ch_2 speed 01, width 00
Slot07: present  No, ch_1 speed 01, width 00, ch_2 speed 01, width 00
Slot08: present Yes, ch_1 speed 03, width 02, ch_2 speed 01, width 00
Ext. Slot-----------------------------------
Con. 01: speed 01, width 00, Type    Upstream
--------------------------------------------
```

When “dual” command is implemented for slots, the showport shows the information for dual port 2x2 per slot

showtype Command

Shows the Back plane board type(U2 or U3) in Active 8 bays JBOF.

Usage: showtype

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>showtype

Backplane type: U2
```

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>showtype

Backplane type: U3
```

Dual Command

Enable dual port control per slot or for all slots.

Usage: dual <slot(D)|all> <on|off>

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>dual all on

Slot 1 dual channel: on
Slot 2 dual channel: on
Slot 3 dual channel: on
Slot 4 dual channel: on
Slot 5 dual channel: on
Slot 6 dual channel: on
Slot 7 dual channel: on
Slot 8 dual channel: on
```

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>dual 1 on

Slot 1 dual channel: on
Slot 2 dual channel: off
Slot 3 dual channel: off
Slot 4 dual channel: off
Slot 5 dual channel: off
Slot 6 dual channel: off
Slot 7 dual channel: off
Slot 8 dual channel: off
```

It requires drive power cycle, hot plug or switch reset to apply the dual port enable/disable setting etc. if only enable/disable “dual port” on slot 1, just power cycle slot 1 or hot-plug the drive slot 1.

If enable/disable “dual port” on all slots, the fastest way is to reset whole JBOF.

buz Command

The command is for controlling the buzzer on switch controller board

Usage: buz <on|off|en|dis>

[en]: enable the buzzer function

[dis]: disable the buzzer function

[on]: set buzzer to beep in one time

[off]: mute buzzer beeping

```
File Edit Setup Control Window Help

Cmd>buz
Buzzer status:disable
Cmd>
```

```
File Edit Setup Control Window Help

Cmd>buz on
OK, turn on buzzer
Cmd>
```

```
File Edit Setup Control Window Help

Cmd>buz off
OK, turn off buzzer
Cmd>
```

```
File Edit Setup Control Window Help

Cmd>buz en
OK, enable buzzer
Cmd>
```

```
File Edit Setup Control Window Help

Cmd>buz dis
OK, turn off buzzer
OK, disable buzzer
Cmd>
```

scan Command

Scan all devices in 8bays Active JBOF

Usage: scan

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Scan I2C channel 0 devices ....
Device address:0xa2 found
Device address:0x40 found
Device address:0x42 found
Device address:0x44 found
Device address:0x46 found
Device address:0x48 found
Device address:0x50 found
Device address:0xd2 found
Device address:0xe0 found
Device address:0x52 found
```

Spread Command

Set the reference clock to Atlas switch and 8 slots to CFC(spread off) or FFC(spread on with -0.5%)

Usage: Spread <on|off>

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>spread
Spread status:On
```

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>spread on
OK, turn on spread
Cmd>spread off
OK, turn off spread
```

It requires switch reset for applying the setting of spread ON or OFF

clk Command

Disable or enable the PCIe reference clock in slots

Usage: clk[en|dis]

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>clk
DIFF0 output enable
DIFF1 output enable
DIFF2 output enable
DIFF3 output enable
DIFF4 output enable
DIFF5 output enable
DIFF6 output enable
DIFF7 output enable
DIFF8 output enable
Cmd>
```

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>clk dis
OK, clock output disable
Cmd>clk
DIFF0 output enable
DIFF1 output disable
DIFF2 output disable
DIFF3 output disable
DIFF4 output disable
DIFF5 output disable
DIFF6 output disable
DIFF7 output disable
DIFF8 output disable
```

Clock DIFF0 is for Atlas switch, it must be always enabled

iicwr Command

Data read for U.2/M.2 drives from SMbus

Usage: iicwr <Addr(H)> <Slot(D)> <ReadByte(D)> <WriteData(H)>

- Addr(H) : Device address
- Slot(D) : Slot should be 1 ~ 8
- ReadByte(D) : Max read byte is 32 byte
- WriteData(D) : Max write byte is 32 byte
- Ex : iicwr d4 1 8 0

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>iicwr d4 1 8 0

Data [0] = 6
Data [1] = 7b
Data [2] = 1f
Data [3] = 1a
Data [4] = 0
Data [5] = 0
Data [6] = 0
Data [7] = 26
```

iicw Command

Byte or page write data to U.2/M.2 drive from SMbus

Usage: iicw <Addr(H)> <Slot(D)> <WriteData(H)...>

- Addr(H) : Device address
- Slot(D) : Slot should be 1 ~ 8
- WriteData(D) : Max write byte is 32 byte
- Ex : iicw d4 1 ff

```
File  Edit  Setup  Control  Window  KanjiCode  Help
Cmd>iicw d4 1 ff

Write Data [0] = ff
```

ver Command

Show S/N, company and model names, the FW version for uP

Usage: ver

```
File  Edit  Setup  Control  Window  KanjiCode  Help
S/N     : 400032001030001
Company : Serial Cables
Model   : ATLAS 8BAY JBOF
Version : 0.1.2     Date : Feb 13 2020 17:23:01
```

eventmark Command

Use for following events mask

Usage: eventmask <event ID> <on|off>

Event ID from 1 to 7

1. Switch Fan Event
2. System Fan1 Event
3. System Fan2 Event
4. Switch Temp Event
5. 12V Voltage Event
6. 1.8V Voltage Event
7. 0.9V Voltage Event

```
File  Edit  Setup  Control  Window  Help

Cmd>eventmask

1.        Switch Fan Event :  enable
2.       System Fan1 Event :  enable
3.       System Fan2 Event :  enable
4.       Switch Temp Event :  enable
5.       12V Voltage Event :  enable
6.      1.8V Voltage Event :  enable
7.      0.9V Voltage Event :  enable
Cmd>
```

```
File  Edit  Setup  Control  Window  Help

Cmd>eventmask 2 off

Set Event Mask success.
Cmd>eventmask

1.        Switch Fan Event :  enable
2.       System Fan1 Event :  disable
3.       System Fan2 Event :  enable
4.       Switch Temp Event :  enable
5.       12V Voltage Event :  enable
6.      1.8V Voltage Event :  enable
7.      0.9V Voltage Event :  enable
Cmd>
```

reset Command

Have fundamental reset in switch board, it resets both of uP and Atlas switch

Usage: reset

```
File  Edit  Setup  Control  Window  Help

Cmd>reset
System Reset...
Cmd>
```