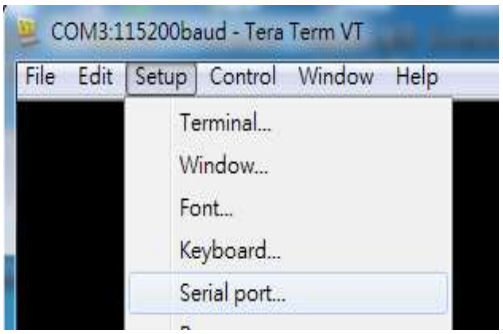
Serial Cables U.2 to M.2 Adapter Setup and CLI Guide

PCI-AD-U2M2-04

Setup

Step 1. Install and launch Tera Term (Hyper Terminal v3.0 and higher is also compatible)

Step 2. To ensure proper communications between the NVMe JBOF controller and the VT100 Terminal emulation, please configure the VT100 Terminal emulation settings to the values shown below:



For “Port”, select COM3 in this example. (Depends on which COM port used on Host)

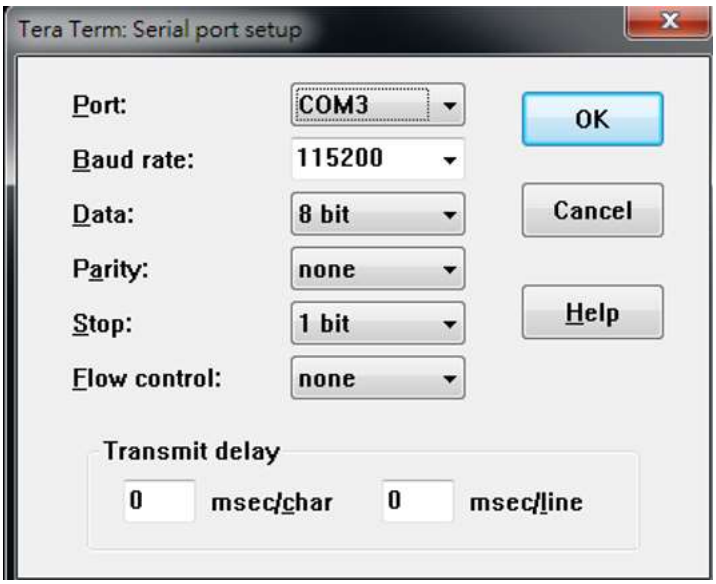
For “Baud rate”, select 115200.

For “Data”, select 8 bit.

For “Parity”, select none.

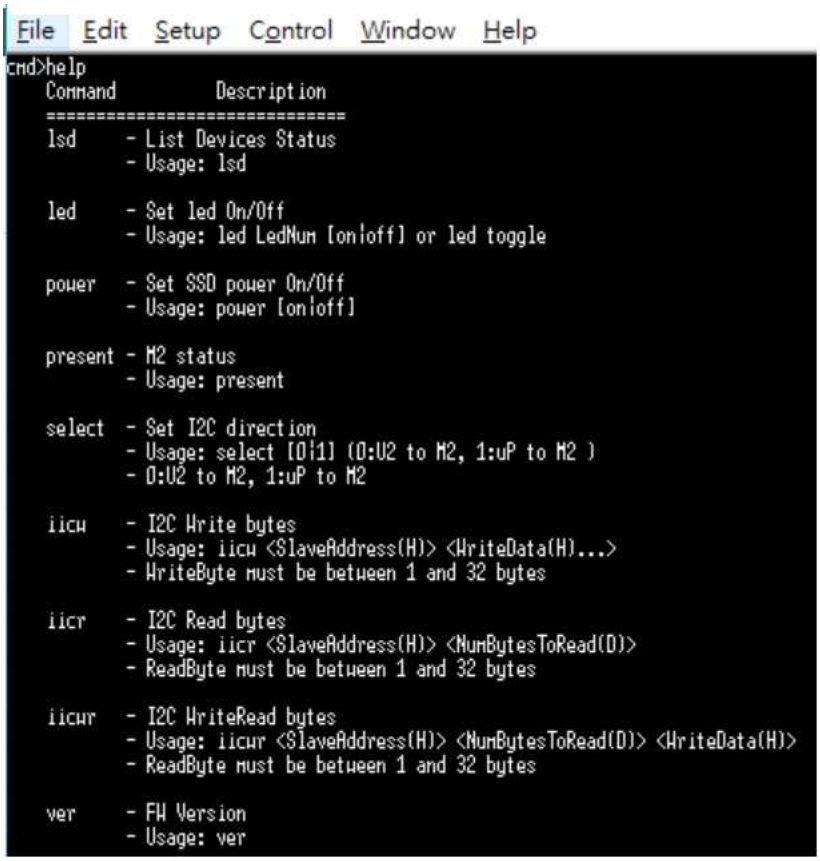
For “Stop”, select 1 bit.

For “Flow control”, select: none.

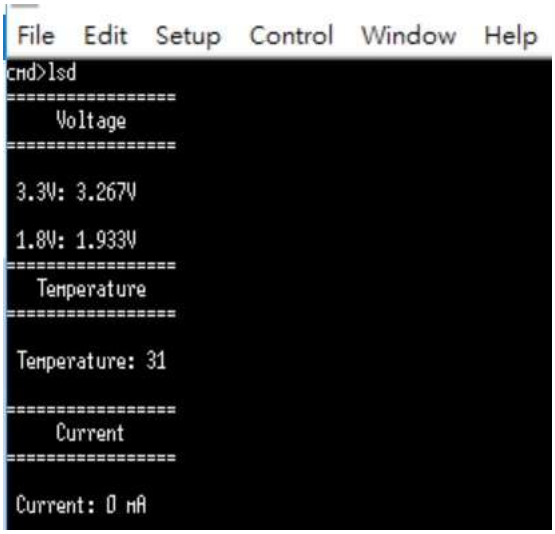


Command Set

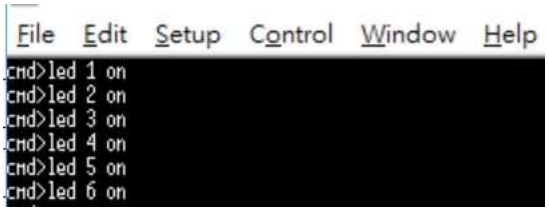
The ‘help’ command shows all commands supported:



The ‘lsd’ command lists the status of the device, along with voltage, current and temperature:



The ‘led’ command shows the status of the two tri-color LEDs located at LED2 and LED3:



The parameter LED1 is used to control Green LED, LED2 is Red, LED3 is blue in “LED2”.

The parameter LED4 is used to control Green LED, LED5 is Red, LED6 is blue in “LED3”.

If you want to turn on the green LED for LED2, type “led 1 on”,

green LED for LED3, type “led 4 on”.

The LED toggle command will enable a staggered power on/off LEDs:

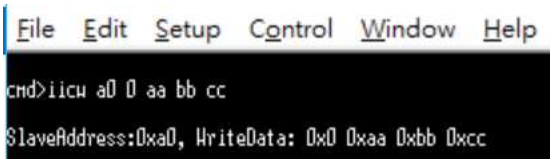
LED1 on→LED1 off→LED2 on→LED2 off

The ‘power’ command toggles the power on/off for the 3.3v pin:

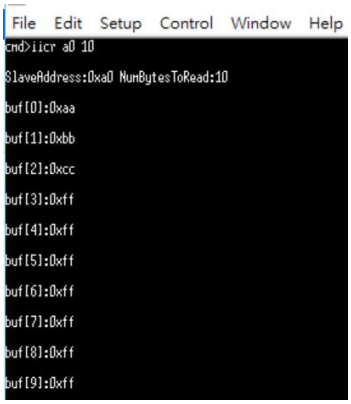
The ‘present’ command displays if a drive was detected:

The ‘select’ command chooses between the two SMBus paths to the M.2 drive, one controlled by uP and the other controlled from the U.2 SFF-8639:

The ‘iicw’ command writes 3 bytes of data to device 0xa0:



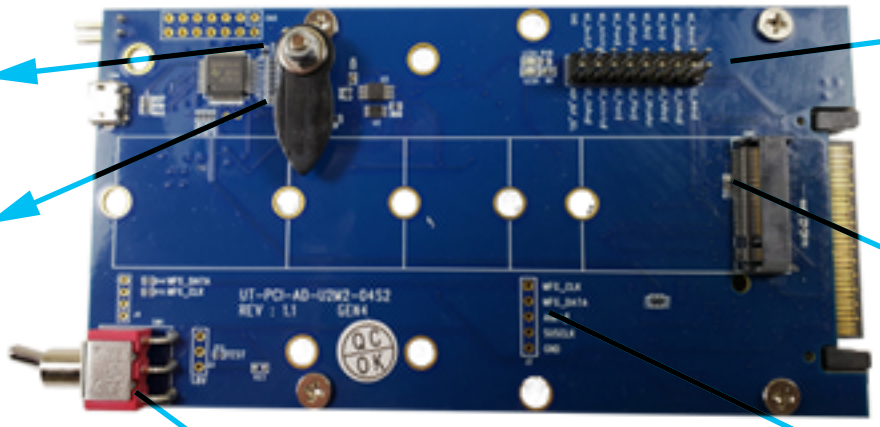
The ‘iicr’ command reads 10 bytes from the device at the a0 address:



The ‘iicwr’ command reads 8 bytes of data from 0xa0, starting from address 0x00:

Note: There is an EEPROM at address 0xA0 for any configuration data storage

FRONT



J1	
U2_WAKE#	M2_WAKE#
U2_CLKREQ#	M2_CLKREQ#
U2_PERST#	M2_PERST#
U2_SPCIERST	
U2_IFDET#	M2_IFDET#
U2_PRSNT#	M2_PRSNT#
U2_ACTIVITY#	M2_ACTIVITY#
U2_CLKREQ#	M2_DEVSLP
MSP_BUF_SEL	GND

J2
MFG_CLK
MFG_DATA
ALETT#
SUSCLK
GND

3.3V for M.2 SSD is
Power on control by uP

Force power on

BACK